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Information Disclosure Statement by Applicant

Applicant: Chung Sun

(Use several sheets if necessary)

Filed: December 18, 2001 Group: 2185

U.S. Patent Documents

Init.		Document No.	Date	Name	Class	Subclass	Filing Date
<i>LA</i>	1	5,477,165	12/19/1995	ElAyat et al.	326	38	12/28/1994
<i>LA</i>	2	5,606,267	02/25/1997	El Ayat et al.	326	41	09/01/1995
<i>LA</i>	3	5,698,992	12/16/1997	El Ayat et al.	326	41	11/13/1996
<i>LA</i>	4	5,936,426	08/10/1999	Wilson et al.	326	49	02/03/1997
<i>LA</i>	5	5,952,852	09/14/1999	McGowan et al.	326	106	07/02/1997

Foreign Documents

Translation

Init.		Document No.	Date	Country	Class	Subclass	Yes	No

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Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)

<i>LA</i>	6	"VariCore™ Embedded Programmable Gate Array Core (EPGA™) 0.18 μm Family" Release 2.0, Actel Corporation, pp. 1-16, June 2001.						

Examiner

[Signature]

Date Considered

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Examiner: Initial if citation considered, whether or not citation is in conference with MPEP 609; Draw line through citation if not conformance and not considered. Include a copy of this form with the next communication to applicant.